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NOTICE OF ALLOWANCE AND FEE(S) DUE

1933

7590

06/25/2009

FRISHAUF, HOLTZ, GOODMAN & CHICK, PC 220 Fifth Avenue
16TH Floor
NEW YORK, NY 10001-7708

EXAMINER					
VLAHOS, SOPHIA					
ART UNIT PAPER NUMBER					

2611

DATE MAILED: 06/25/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/579,385	05/11/2006	Satoru Shiratsuchi	06299/LH	8214

TITLE OF INVENTION: DIGITAL SIGNAL OFFSET ADJUSTING APPARATUS AND PULSE PATTERN GENERATOR USING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	09/25/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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							(Signature)
							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTOR	NEY DOCKET NO.	CONFIRMATION NO.
10/579,385	05/11/2006	•	Satoru Shiratsuchi			06299/LH	8214
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nonprovisional	NO	\$1510	\$300	\$0		\$1810	09/25/2009
EXAM	INER	ART UNIT	CLASS-SUBCLASS				
VLAHOS.	, SOPHIA	2611	375-239000	•			
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			The Director is hereby overpayment, to Depo	sit Account Number	r	(enclose a	n extra copy of this form).
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10/579,385	05/11/2006 Satoru Shiratsuchi		06299/LH	8214
1933 75	90 06/25/2009		EXAM	INER
FRISHAUF, HO	LTZ, GOODMAN &	VLAHOS, SOPHIA		
220 Fifth Avenue			ART UNIT	PAPER NUMBER
16TH Floor NEW YORK, NY	10001-7708	2611 DATE MAILED: 06/25/200	9	

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 629 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 629 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)
	10/579,385	SHIRATSUCHI ET AL.
Notice of Allowability	Examiner	Art Unit
	SOPHIA VLAHOS	2611
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not included will be mailed in due course. THIS
1. X This communication is responsive to <u>5/14/09</u> .		
2. ☑ The allowed claim(s) is/are <u>1-20</u> .		
 3. Acknowledgment is made of a claim for foreign priority una) a) All b) ☐ Some* c) ☐ None of the: 1. Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Application No	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements
 A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give 		
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) ☐ including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTO-	948) attached
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the C	Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
6. DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendr 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), e

Art Unit: 2611

DETAILED ACTION

Specification

1. The revised abstract received on 5/14/09 is acceptable

Allowable Subject Matter

2. The following is a statement of reasons for the indication of allowable subject matter: The prior art of the record fails to teach or suggest alone or in combination: A digital signal offset adjusting apparatus comprising: an operational amplifier, a first input end of which is connected to the other end of the first coil, a second input end of which is connected to the direct current voltage generator, an output end of which is connected to another end of the second coil, and which outputs to the output terminal via the other end of the second coil from the output end, the low frequency band of the input digital signal passed to the other end of the first coil input to the first and second input ends and a composite signal obtained by combining the direct current component and the direct current bias voltage output from the direct current voltage generator; and a frequency characteristic compensating circuit connected between a reference electrical potential point and the second input end of the operational amplifier or between the second input end and the output end, the compensating circuit being adopted to compensate for a frequency characteristic so that a gain of the operational amplifier increases with a component having a higher frequency from among the low frequency bands of the input digital signal passed to the other end of the first coil, as recited in claim 1 and in combination with other elements of the claim.

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Claims 1-5 are allowed over prior art.

The prior art of the record fails to teach or suggest alone or in combination: A pulse pattern generator comprising: a digital signal offset adjusting apparatus, wherein the digital signal offset adjusting apparatus comprises: an operational amplifier, a first input end of which is connected to the other end of the first coil, a second input end of which is connected to the direct current voltage generator, an output end of which is connected to another end of the second coil, and which outputs to the output terminal via the other end of the second coil from the output end, the low frequency band of the input digital signal passed to the other end of the first coil input to the first and second input ends and a composite signal obtained by combining the direct current component and the direct current bias voltage output from the direct current voltage generator; and a frequency characteristic compensating circuit connected between a reference electrical potential point and the second input end of the operational amplifier or between the second input end and the output end, the compensating circuit being adopted to compensate for a frequency characteristic so that a gain of the operational amplifier increases with a component having a higher frequency from among the low frequency bands of the input digital signal passed to the other end of the first coil, as recited in claim 11 and in combination with other elements of the claim

Claims 11-15 are allowed over prior art.

The prior art of the record fails to teach or suggest alone or in combination: A digital signal offset adjusting apparatus comprising: a first operational amplifier, a first input end of which is connected to the other end of the first coil, a second input end of which is connected to a reference electrical potential point, and which outputs from an output end a first inverted and amplified signal obtained by inverting and amplifying the low frequency band and the direct current component of the input digital signal passed to the other end of the first coil; a second operational amplifier, a first input end of which is connected to the direct current voltage generator, a second input end of which is connected to the reference electrical potential point, and which outputs from an output end a second inverted and amplified signal obtained by inverting and amplifying the direct current bias voltage output from the direct current voltage generator; a third operational amplifier, a first input end of which is connected in common to each of the output ends of the first and second operational amplifiers, a second input end of which is connected to the reference electrical potential point, and which inverts and amplifies a combined signal obtained by combining the first and second inverted and amplified signals and outputs the inverted and amplified signal to the other end of the second coil; and first and second frequency characteristic compensating circuits connected between the reference electrical potential point and each of the first input end of the first and third operational amplifiers or between each of the first input end and the output end of the first and third operational amplifiers, the first and second frequency characteristic

compensating circuits being adopted to compensate for a frequency characteristic so that a gain of each of the first and third operational amplifiers increases with a component having a higher frequency from among the low frequency bands of the input digital signal passed to the other end of the first coil, as recited in claim 6 and in combination with other elements of the claim.

Claims 6-10 are allowed over prior art

The prior art of the record fails to teach or suggest alone or in combination: A pulse pattern generator comprising: a digital signal offset adjusting apparatus, wherein the digital signal offset apparatus comprises: a first operational amplifier, a first input end of which is connected to the other end of the first coil, a second input end of which is connected to a reference electrical potential point, and which outputs from an output end a first inverted and amplified signal obtained by inverting and amplifying the low frequency band and the direct current component of the input digital signal passed to the other end of the first coil; a second operational amplifier, a first input end of which is connected to the direct current voltage generator, a second input end of which is connected to the reference electrical potential point, and which outputs from an output end a second inverted and amplified signal obtained by inverting and amplifying the direct current bias voltage output from the direct current voltage generator; a third operational amplifier, a first input end of which is connected in common to each of the output ends of the first and second operational amplifiers, a second input end of which

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is connected to the reference electrical potential point, and which inverts and amplifies a combined signal obtained by combining the first and second inverted and amplified signals and outputs the inverted and amplified signal to the other end of the second coil; and first and second frequency characteristic compensating circuits connected between the reference electrical potential point and each of the first input end of the first and third operational amplifiers or between each of the first input end and the output end of the first and third operational amplifiers, the first and second frequency characteristic compensating circuits being adopted to compensate for a frequency characteristic so that a gain of each of the first and third operational amplifiers increases with a component having a higher frequency from among the low frequency bands of the input digital signal passed to the other end of the first coil, as recited in claim 16 and in combination with other elements of the claim.

Claims 16-20 are allowed over prior art.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Ohkawa (U.S. 3,950,711) discloses a frequency response controlling circuit.

Oppelt (U.S. 6,573,788) discloses an amplifier device with pulse shaping circuitry.

Contact Information

Art Unit: 2611

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SOPHIA VLAHOS whose telephone number is (571)272-5507. The examiner can normally be reached on MTWRF 8:30-17:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammed Ghayour can be reached on 571 272 3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/SOPHIA VLAHOS/ Examiner, Art Unit 2611 6/25/2009

/Mohammad H Ghayour/ Supervisory Patent Examiner, Art Unit 2611